I hereby certify that this correspondence is being deposited with the United States Postal Service addressed to: Commissioner of Patents and Trademarks, Alexandria, VA 22313, on May 4, 2004. The applicant and/or attorney requests the date of deposit as the filling date. Depositor: Karen Cinq-Mars

(Signature & date)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

May 4, 2004

Khater, et al.

Group Art Unit: To Be Assigned

Serial No: 10/709,220

Examiner: To Be Assigned

Filed: 4/22/04

International Business Machines Corporation

2070 Route 52

Hopewell Junction, NY 12533

TITLE:

STRUCTURE AND METHOD OF FORMING BIPOLAR TRANSISTOR HAVING A SELF-ALIGNED RAISED EXTRINSIC BASE USING SELF-ALIGNED ETCH STOP LAYER

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to the duty of disclosure set forth in 37 C.F.R. 1.56, and further pursuant to the provisions of 37 C.F.R. 1.97 and 1.98, applicants hereby respectfully submit copies of the non-US patents and publications as listed on Form PTO-1449, attached hereto.

In citing these documents, no representation is made nor intended as to the pertinency or nonpertinency of the art, that better art than that listed is not available, or that other art is not applicable.

No fee is believed to be due for this submission. If any fees are required, however, the Commissioner is hereby authorized to charge such fees to Deposit Account No. 09-0458.

Respectfully submitted,

Khater, et al.

H. Daniel Schnurmann

Registration No. 35,791

Telephone No. 845-894-2481

10/709,220 FIS920030412US1

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT AF								FIS920030412US1						
								APPLICANT:						
								KHATER ET AL.						
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		A Jagannathan, et al., "Self-aligned SiGe NPN Transistors with 285 GHz fMAX and 207 GHz fT in a Manufacturable Technology," IEEE Electron Device Letters 23, 258 (2002)												
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